Z8601700ZC0
PCMCIA INTERFACE DEVELOPMENT KIT

GENERAL DESCRIPTION

The Z8601700ZCO Development Kit allows easy evaluation of the functions and capabilities of the Z86017 PCMCIA Interface Adapter. The board provides a ZIF socket for easy insertion and removal of the Z86017, as well as a full pin-out header for access to all signals. The board also provides breadboard space to assist in development.

Power to the Z86017 can either be supplied through a common point for current measurements applied through the interface, or supplied through a separate power connector. Programming the Z86017 is accomplished through the SPI port of a microcontroller or through the on-board EEPROM. The main clock source is supplied through the interface or through the on-board clock.

SUPPORTED DEVICES

Z86017

Specifications
Power Requirements
$3.0V < V_{CC} < +5.5V$

Dimensions
Length: 7.6 in. (19.3 cm)

Width: 4.5 in. (11.4 cm)

KIT CONTENTS

Evaluation Board

■ Z86017 PCMCIA Interface Adapter Device

■ 100-Pin VQFP ZIF Socket

■ 256 X 8-Byte EEPROM

■ 20 MHz Oscillator

■ Headers for full Z86017 pin-out

■ Headers for access for PCMCIA signals

■ Headers for connection to AT-Bus (on both Host and ATA side)

■ Headers for connection to PCMCIA Extender Card

■ Header for intelligent peripheral programming of EEPROM

■ Power Connector

ZPCMCIA0ZDP PCMCIA Extender Card

Cables

■ Two 6-inch, 34-pin IDC to 34-pin IDC Cables

■ Power Cable with 1.0A Fuse

■ Power Cable with Banana Plugs

Software

■ Example Initialization Code

Documentation

■ Z8601700ZCO Evaluation Kit User's Manual

■ Z86017/Z16017 Reference Manual

■ Product Registration Card